



**MADANAPALLE INSTITUTE OF TECHNOLOGY & SCIENCE, MADANAPALLE- 517 325**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING – Time Table for 2024-25**  
**I.M.TECH II SEMESTER (VLSI Design and Embedded Systems)**



Class In Charge: Dr. G. Reddy Hemantha

With effect from: 17/03/2025

Class Room: EB – 218A

DAY/TIME	10.10-11.10	11.10 -12.10	12.10-01.10	1.10 – 2.00	2.00 – 3.00	3.00 – 4.00	4.00 – 5.00
MON	PRML	CMOS A Lab		B R E A K	ESD	Technical Seminar	
TUE	ERPW	PRML	CMOS A		PDA	ESD Lab	
WED	Technical Seminar		ERPW		CMOS A	Holistic Activities	
THU	ESD	CMOS A Lab			PDA	Holistic Activities	
FRI	PRML	CMOS A	PDA		ESD	ESD Lab	
SAT	Holistic Activities				Holistic Activities		

SUBJECTS				
PCC	CMOS A	24VESP103	CMOS Analog IC Design	Dr. G. Reddy Hemantha
PCC	ESD	24VESP104	Embedded System Design	Dr. S. Rajasekaran
PE-III	PRML	24VESP407	Pattern Recognition and Machine Learning	Dr. K. Sateesh
PE-IV	PDA	24VESP412	Physical Design Automation	Dr. H. Shree Kumar
PCC	CMOS A Lab	24VESP203	CMOS Analog IC Design Laboratory	Dr. G. Reddy Hemantha & Dr. R. Ravindraiah
PCC	ESD Lab	24VESP204	Embedded System Design Laboratory	Dr. S. Rajasekaran & Ms. Sai Anusha (Tue), Mr. Veeraiah Maddu (Fri)
PR	Technical Seminar	24VESP701	Technical Seminar	Dr. H. Shree Kumar & Mr. Asif Moiz
AC	ERPW	24AUP903	English for Research Paper Writing	Dr. Agnibha Das Majumdar

Time Table coordinator

HOD

Principal